L Number	Hits	Search Text	DB	Time stamp
-	634		USPAT;	2004/05/19 12:18
	001		US-PGPUB	-301,00,13 12.10
_	75	703/14.ccls. and (programmable adj logic	USPAT;	2004/05/19 12:19
		adj device or pld or field adj	US-PGPUB	2001,00,13 12.13
		programmable adj gate adj array or fpga)	00 10100	
l _	0	quccione-steven.in.	USPAT;	2004/05/19 14:46
_	v	guccione steventin.	US-PGPUB	2004/03/13 14.40
1_	35	quccione.in.	USPAT;	2004/05/19 16:25
-	33	guccione.in.	US-PGPUB	2004/05/19 10.25
l _	0	mcmillan-s.in.	USPAT;	2004/05/19 16:21
-	v	mentitan 3.in.	US-PGPUB	2004/03/13 10.21
l	3	"6510546"	USPAT;	2004/05/20 07:55
-	5	0310340	US-PGPUB	2004/03/20 07.33
l _	519	716/11.ccls.	USPAT;	2004/05/20 09:19
-	319	710711.0013.	US-PGPUB	2004/03/20 03.13
l _	515	716/11.ccls. and (FPGA or field	USPAT;	2004/05/20 07:56
-	313	programmable gate array or PLD or	US-PGPUB	2004/05/20 07.50
		programmable logic device)	05-16105	
l _	30	(716/11.ccls. and (FPGA or field	USPAT;	2004/05/20 07:57
_	50	programmable gate array or PLD or	US-PGPUB	2004/03/20 07.57
1		programmable logic device)) and	05-16105	
		(reconfigurable or reconfiguration or		
		configurable)		
i	395		USPAT;	2004/05/20 09:19
-	333	710/10.0013.	US-PGPUB	2004/03/20 09.19
i	236	716/16.ccls. and (fpga or field adj	USPAT;	2004/05/20 09:20
-	230	programmable adj gate adj array)	US-PGPUB	2004/03/20 09.20
i_	181	(716/16.ccls. and (fpga or field adj	USPAT;	2004/05/20 09:20
-	101	programmable adj gate adj array)) and	US-PGPUB	2004/03/20 03.20
		(reconfigurable or configurable or	05-10100	
		reconfiguration)		
i _	54		USPAT;	2004/05/20 09:20
<u> </u>	34	programmable adj gate adj array)) and	US-PGPUB	2004/03/20 03.20
		(reconfigurable or configurable or	OD IGIOD	
		reconfiguration)) and simulation		
<u> </u>	35	quccione.in.	USPAT;	2004/05/21 12:15
	33		US-PGPUB	-301,03,21 12.13
_	0	guccione.in. and queue	USPAT;	2004/05/21 12:15
			US-PGPUB	-301,00,21 12.10
-	1	guccione.in. and event	USPAT;	2004/05/21 12:16
	•		US-PGPUB	-301,00,21 12.10
-	87	event adj driven adj simulation	USPAT	2004/05/21 12:17
_	3456		USPAT	2004/05/21 12:16
	5.50	or PLD		
_	14	(event adj driven adj simulation) and	USPAT	2004/05/21 12:26
		(FPGA or PLD)		-301, 50, 51
_	12	((event adj driven adj simulation) and	USPAT	2004/05/21 13:05
		(FPGA or PLD)) and (queue or LIFO or		
]		asynchronous or synchronous)		
Ц		1		L